

WHAT IS CLAIMED IS:

1. A solid-state image pickup apparatus
comprising:

5 a pixel including a photoelectric conversion unit,
a read transistor for reading a signal from said
photoelectric conversion unit, and a reset transistor
for resetting an input portion of said read transistor;
and

10 an output line to which the signal from the read
transistor is read out,

wherein the reset transistor is controlled in
accordance with a signal level of said output line.

15 2. A solid-state image pickup apparatus
comprising:

a plurality of pixels arrayed in horizontal and
vertical directions, each pixel including a
photoelectric conversion unit, a read transistor for
reading a signal from the photoelectric conversion
20 unit, and a reset transistor for resetting an input
portion of the read transistor, wherein the reset
transistor is turned on/off by controlling a control
electrode area thereof; and

25 a signal line for supplying a predetermined signal
level to operate the read transistor,

wherein the signal line is connected to one of
main electrode areas of said reset transistor, and

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the signal line independently supplies a signal on a unit basis of said plurality of pixels in a horizontal direction.

5 3. An apparatus according to claim 1, further comprising a control transistor connected to said output line to control the signal level of said output line.

10 4. A apparatus according to claim 2, further
comprises an output line arranged on the unit basis of
said plurality of pixels in the horizontal direction,
to which a signal from the read transistor is read out,
wherein the reset transistor is controlled in
15 accordance with a signal level of said output line.

5. A apparatus according to claim 2, wherein the read transistor amplifies and outputs a signal in the control electrode area from one of the main electrode areas, and wherein said signal line is connected to an other main electrode area of the read transistor.

6. A apparatus according to claim 1, wherein the read transistor and reset transistor are MOS transistors.

7. A apparatus according to claim 2, wherein the

read transistor and reset transistor are MOS transistors.

8. A apparatus according to claim 1, further
comprises a transfer switch arranged between the
photoelectric conversion unit and the read transistor,
wherein signal charges accumulated in the
photoelectric conversion unit are transferred to the
input portion of the read transistor through said
transfer switch.

9. A apparatus according to claim 2, further comprises a transfer switch between the photoelectric conversion unit and the read transistor,

15 wherein signal charges accumulated in the photoelectric conversion unit are transferred to the input portion of the read transistor through said transfer switch.

10. A apparatus according to claim 1, wherein a plurality of transfer switches are connected to the input portion of the read transistor, and signal charges are independently transferred from a plurality of photoelectric conversion units by said transfer switches.

11. A apparatus according to claim 2, wherein a

plurality of transfer switches are connected to the
input portion of the read transistor, and signal
charges are independently transferred from a plurality
of photoelectric conversion units by said transfer
5 switches.

12. An image pickup system comprising:
said solid-state image pickup apparatus of claim
1;
10 an optical system adapted to form an image of
light onto said solid-state image pickup apparatus; and
a signal processing circuit adapted to process an
output signal from said solid-state image pickup
apparatus.

13. An image pickup system comprising:
said solid-state image pickup apparatus of claim
2;
an optical system adapted to form an image of
20 light onto said solid-state image pickup apparatus; and
a signal processing circuit adapted to process an
output signal from said solid-state image pickup
apparatus.